

MULTILAYER VARISTORS

Transient Voltage Surge Suppressors

Applications

- Suppression of circuit board level Surge, ESD, EFT and other transient events
- ESD protection for components sensitive to IEC 61000-4-2, MIL-STD-883C Method 3015.7, and other industry specifications
- Provides on-board transient voltage protection for ICs and Transistors
- Used to help achieve Electromagnetic Compliance of end products
- Replace larger surface mount TVS zeners diodes in many applications

Features

- Thinlayer and high precise technique
- Able to withstand high surge current
- Bi-directional clamping characteristic
- Standard / Low capacitance chip TVS types available
- Available with Nickel / Tin end terminations

Specification

- Operating Temperature ● Solderability
-55 to +125°C 260°C 2sec (IEC 60068-2-58)
- Soldering Heat Resistance
260°C 5sec (IEC 60068-2-58)
- Response time < 0.5ns
- Temperature coefficient (αV) of clamping Voltage (V_c)
@ specified test current <0.01% / °C
- Leadless 0402, 0603, 0805, 1206, 1210, 1812, 2220 chip sizes
- Operating voltage range (DC) available from 3.3V to120V
- Withstand ESD durability test severity of IEC 61000-4-2 level 4

PRODUCT NUMBER SYSTEM

JV 10 ML 30 541 P T □

1 2 3 4 5 6 7 8

1=Series Code

JV : High Power Series

2=Size code

10 : EIA 1210

3=Type Code

ML : Multilayer

4=Rated Voltage Code

30 : 30 VM (DC)

5=Capacitance

541 : $54 \times 10^1 = 540$ pF Typical

6=End Termination

P : Nickle/Tin plated

7=Packing Code

T : Tape & Reel

B : Bulk

8=Special Code

DEVICE RATINGS AND SPECIFICATIONS

PART NUMBER	Maximum Ratings(125°C)					Specifications(25°C)		
	Maximum Continuous Working Voltage		Maximum Non- Repetitive Surge Current (8/20µs)	Maximum Non- Repetitive Surge Energy (10/1000µs)	Maximum Clamping Voltage at Specified Current (8/20µs)	Nominal Voltage at 1 mA (DC) Test Current		Typical Capacitance @1 MHz
	V _{M(DC)}	V _{M(AC)}	I _{TM}	W _{TM}	V _c	V _{N(DC)} min.	V _{N(DC)} max.	C
	(V)	(V)	(A)	(J)	(V)	(V)	(V)	(pF)
JV10ML30541PT	30	25	200	0.9	65 at 10 A	35.0	43.0	540
JV10ML30931PT	30	25	280	1.2	62 at 10 A	35.0	43.0	930
JV10ML38701PT	38	30	250	2.0	78 at 10 A	42.3	51.7	700
JV10ML56331PT	56	40	250	2.3	110 at 10 A	61.2	74.0	330

Notes :

1、Maximum ESD clamp voltage tested with IEC 61000-4-2 Human Body Model discharge test current and direct discharge to device terminal (IEC preferred test method) .

2、Typical leakage current < 5µA .

3、Capacitance may be customized , please contact factory for availability .